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Application Number	10/079472
Filing Date	February 19, 2002
	MAITREYEE MAHAJANI
Title	Gate Dielectric Structures for Integrated Circuits and Methods for Making and Using Such Gate Dielectric
Group Art Unit	Structures 2814
Examiner Name	Thao X. Le
Attorney Docket Number	MA-068

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Assignee of record of the entire interest, See 37 CFR 3.71.  Statement under 37 CFR 3.73(b) is enclosed. (Form PTO/SB/96).					
		of Applicant or Assig			
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forms if more than one signature is required, see below.					
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STATEMENT UNDER 37 CFR 3.73(b)				
Applicant/Patent Owner:_Maitreyee Mahajan	<u> </u>			
Application No./Patent No.: 10/079472	Filed/Issue Date: February 19, 2002			
Entitled: Gate Dielectric Structures for Integral	ted Circuits and Methods for Making and Using Such Gate Dielectric Structu			
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